Schedule Trees

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Outline

Introduction

- Example
- Single Statement
- Multiple Statements
- Schedule Trees

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Advantages

- Useful in several contexts
- More natural
- More convenient
- More expressive
- Extensible



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Conclusion

for	(i =	0; i <= N; ++i)
S :	a[i]	= g(i);
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Т:	b[i]	= f(a[N-i]);



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 - Iteration domain

 $\{ S[i] : 0 \le i \le N; T[i] : 0 \le i \le N \}$

• Dependences

$$\{ \mathbf{S}[i] \to \mathbf{T}[N-i] : 0 \le i \le N \}$$

- Execution Order
 - Original Order

 $S[0], S[1], S[2], \dots, S[N - 1], S[N], T[0], T[1], T[2], \dots, T[N - 1], T[N]$

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Alternative Order

S[0], T[N], S[1], T[N - 1], S[2], T[N - 2], ..., S[N - 1], T[1], S[N], T[0]

for (i = 0; i <= N; ++i)
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for (i = 0; i <= N; ++i) {
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Expressing Transformations (Single Statement)

for (i = 0; i <= N; ++i) $b[i] = f(a[N-i]); \Rightarrow$ for (i = 0; i <= N; ++i) b[N-i] = f(a[i]);

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Two approaches

Modify Iteration Domain

 $\mathbf{T}[i] \to \mathbf{T}'[N-i]$

- iteration domains have implicit execution order (lexicographic order)
- AST generator takes modified iteration domain as input
- access relations and dependence relations are adjusted accordingly

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$\mathbf{T}[i] \rightarrow [N-i]$

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- execution order is determined by schedule space (lexicographic order)
- AST generator takes iteration domain and schedule as input
- ► schedule is typically a piecewise quasi-affine function

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Representing Schedules for Multiple Statements for (i = 0; i <= N; ++i) for (i = 0; i <= N; ++i) { a[i] = g(i);a[i] = g(i);} b[i] = f(a[N-i]); $S[i] \rightarrow [i]; T[i] \rightarrow [N-i]$ first S[i] then T[i] $S[i] \rightarrow [i] T[i] \rightarrow [i]$ first S[i] then T[i]

Representing Schedules for Multiple Statements for $(i = 0; i \le N; ++i)$ for $(i = 0; i \le N; ++i)$ a[i] = q(i);a[i] = q(i);for (i = 0; i <= N; ++i)</pre> b[N-i] = f(a[i]):} b[i] = f(a[N-i]); $S[i] \rightarrow [i]; T[i] \rightarrow [N-i]$ first S[i] then T[i] $S[i] \rightarrow [i] \qquad T[i] \rightarrow [i]$ first S[*i*] then T[*i*] $S : \{ [i] \rightarrow [0, i] \}$ $S : \{ [i] \rightarrow [i, 0] \}$ Kelly $T:\{[i] \rightarrow [1,i]\}$ $T: \{[i] \rightarrow [N-i,1]\}$

⇒ encode statement ordering in affine function

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Representing Schedules for Multiple Statements for $(i = 0; i \le N; ++i)$ for $(i = 0; i \le N; ++i)$ { a[i] = q(i);a[i] = q(i);for (i = 0; i <= N; ++i)</pre> b[N-i] = f(a[i]);} b[i] = f(a[N-i]); $S[i] \rightarrow [i]; T[i] \rightarrow [N-i]$ first S[i] then T[i] $S[i] \rightarrow [i] \qquad T[i] \rightarrow [i]$ first S[*i*] then T[*i*] $S : \{ [i] \rightarrow [0, i] \}$ $S: \{[i] \rightarrow [i, 0]\}$ Kelly $T:\{[i] \rightarrow [1,i]\}$ $T: \{[i] \rightarrow [N-i,1]\}$ union $\{ S[i] \rightarrow [0, i]; T[i] \rightarrow [1, i] \}$ $\{ S[i] \rightarrow [i, 0]; T[i] \rightarrow [N - i, 1] \}$ map \Rightarrow encode statement ordering in affine function

Representing Schedules for Multiple Statements for $(i = 0; i \le N; ++i)$ for $(i = 0; i \le N; ++i)$ { a[i] = g(i);a[i] = g(i);for (i = 0; i <= N; ++i)</pre> b[N-i] = f(a[i]);} b[i] = f(a[N-i]);sequence $S[i] \rightarrow [i]; T[i] \rightarrow [N-i]$ S[*i*] T[*i*] sequence schedule tree S[*i*] T[*i*] $S[i] \rightarrow [i] \qquad T[i] \rightarrow [i]$ $S: \{[i] \rightarrow [0, i]\}$ $S : \{ [i] \rightarrow [i, 0] \}$ Kelly $T:\{[i] \rightarrow [1,i]\}$ $T: \{[i] \rightarrow [N-i, 1]\}$ union $\{\mathbf{S}[i] \rightarrow [0, i]; \mathbf{T}[i] \rightarrow [1, i]\}$ $\{S[i] \rightarrow [i, 0]; T[i] \rightarrow [N - i, 1]\}$ map

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Schedule Trees



- Band: multi-dimensional piecewise quasi-affine partial schedule
- Filter: selects statement instances that are executed by descendants
- Sequence: children executed in given order
- Set: children executed in arbitrary order

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- "External" node types
 - Domain: set of statement instances to be scheduled
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- "External" node types
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 - Context: external constraints on symbolic constants
- Convenience node types
 - Mark: attach additional information to subtrees
 - Leaf: for easy navigation

Comparison

- sequence $T_1: \{[i] \rightarrow [0, i]\}$]} $S_1[i]$ $T_2:\{[i,j] \rightarrow [1,j, 0,i]\}$
- $\{S_1[i] \rightarrow [0, i, 0, 0];$ $S_2[i, j] \rightarrow [1, j, 0, i];$ $S_3[i] \rightarrow [1, i-1, 1, 0]$
 - Kelly's abstraction
 - schedule spread over statements
 - relaxed lexicographic order
 - union maps
 - single object
 - strict lexicographic order
 - schedule transformations can be composed
 - schedule trees
 - single object
 - relaxed lexicographic order

 $S_2[i, j]; S_3[i]$ $T_3: \{[i] \longrightarrow [1, i-1, 1]\} \quad S_1[i] \rightarrow [i] \quad S_2[i, j] \rightarrow [j]; S_3[i] \rightarrow [i-1]$ sequence $S_2[i, j] = S_3[i]$ $S_2[i, j] \rightarrow [i]$

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Schedule Uses

- Representing the original execution order
 - Input to dependence analysis (in isl)
 - Basis for manual/incremental transformations
- Scheduling
 - Construction based on dependences
 - Schedule modifications
- AST generation
 - Generate AST from schedule

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Schedule Trees Everywhere



Schedule Trees Everywhere



New PPCG: C code \rightarrow parse \rightarrow schedule tree \rightarrow dependence analysis tile \leftarrow schedule tree \leftarrow scheduler \leftarrow dependences schedule tree \rightarrow AST generator \rightarrow AST

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Schedule Construction Example

• Iteration domain

 $\{ S[i] : 0 \le i \le N; T[i] : 0 \le i \le N; U[] \}$

Dependences

 $\{ \mathbf{S}[i] \to \mathbf{T}[N-i] : 0 \le i \le N \}$

for (i = 0; i <= N; ++i)
S:
$$a[i] = g(i);$$

for (i = 0; i <= N; ++i)
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U: c = 0;
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S[i] T[i]



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 $\{S[i] \to T[N-i]: 0 \le i \le N\}$
 $S[i] = T[i]$
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Schedule Construction Example



 \Rightarrow natural representation of constructed schedule

Local Transformations

Typical scenario:

- Construct tilable bands (e.g., using Pluto algorithm)
- Individually tile (some) tilable bands
 - ▶ Given a band $D(i) \rightarrow f(i)$, insert a band $D(i) \rightarrow \lfloor f(i)/S \rfloor$
 - First iterate over blocks of size **S** and then iterate within each block

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 - bands of different dimensionality
 - different tile sizes S per band

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First iterate over blocks of size S and then iterate within each block Tiled individually:

- bands of different dimensionality
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$$\begin{array}{c} \text{set} \\ S_{2}[i,j,k] & \\ S_{2}[i,j,k] \rightarrow (k,j) \\ \\ S_{2}[i,j,k] \rightarrow (i) \end{array} \qquad \begin{array}{c} \text{set} \\ S_{1}[i,j]; S_{3}[i,j,k] \\ \\ S_{1}[i,j]; S_{3}[i,j,k] \rightarrow (\lfloor i/S_{0} \rfloor, \lfloor j/S_{1} \rfloor, 0); \\ \\ S_{3}[i,j,k] \rightarrow (\lfloor i/S_{0} \rfloor, \lfloor j/S_{1} \rfloor, \lfloor k/S_{2} \rfloor) \\ \\ \\ S_{1}[i,j] \rightarrow (i,j,0); \\ \\ S_{3}[i,j,k] \rightarrow (i,j,k) \\ \end{array}$$

Local Transformations



Local Transformations



 $T_{1} : \{ [i, j] \rightarrow [1, i, j, 0] \}$ Kelly's abstraction: $T_{2} : \{ [i, j, k] \rightarrow [0, k, j, i] \}$ $T_{3} : \{ [i, j, k] \rightarrow [1, i, j, k] \}$

How to identify node that needs to be tiled?

interval of dimensions

list of statements or values for set/sequence encodings

Local Transformations



$$T_{1} : \{ [i, j] \to [1, i, j, 0] \}$$

Kelly's abstraction: $T_{2} : \{ [i, j, k] \to [0, k, j, i] \}$
 $T_{3} : \{ [i, j, k] \to [1, i, j, k] \}$

How to identify node that needs to be tiled?

interval of dimensions

• list of statements or values for set/sequence encodings

Union map representation additionally requires alignment of single schedule space

CARP Project

Design tools and techniques to aid Correct and Efficient Accelerator Programming



Advanced Use: CUDA/OpenCL Code Generation

Schedule tree logically split into two parts

- Outer part mapped to host code
- Subtrees mapped to device code
- Device part has additional symbolic constants
 - ⇒ block and thread identifiers
 - ⇒ internal context nodes
- Each thread executes only part of iteration domain
 - \Rightarrow selected using filter nodes

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Old PPCG used nested AST generation

 \Rightarrow difficult to understand and debug

Advanced Use: CUDA/OpenCL Code Generation

for (t = 0; t < T; t++) {
for (i = 1; i < N - 1; i++)
B[i] = 0.33333 * (A[i-1] + A[i] + A[i + 1]);
for (j = 1; j < N - 1; j++)
A[j] = B[j];
S[t, i]
$$\rightarrow$$
 [t]; t[t, j] \rightarrow [t]
S[t, i] \rightarrow [0]; t[t, j] \rightarrow [1]
mark: kernel
 $T[t, j]$
mark: kernel
 $0 \le b < 32768 \land 0 \le t < 32$
T[t, j] : $b = \lfloor j/32 \rfloor \mod 32768$
T[t, j] $\rightarrow \lfloor j/32 \rfloor$
T[t, j] : $t = j \mod 32$
T[t, j] $\rightarrow j \mod 32$
S[t, i] $\rightarrow i \mod 32$
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for (j = 1; j < N - 1; j++)
A[j] = B[j];
subtree mapped to device

$$S[t, i] \rightarrow [0]; t[t, j] \rightarrow [1]$$

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 $T[t, j] \rightarrow j \mod 32$
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 $S[t, i] \rightarrow [t] \rightarrow [t] \mod 32$

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for (t = 0; t < T; t++) {
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B[i] = 0.33333 * (A[i-1] + A[i] + A[i + 1]);
for (j = 1; j < N - 1; j++)
A[j] = B[j];
introduce identifiers

$$S[t, i] \rightarrow [0]; t[t, j] \rightarrow [1]$$

 set
 $T[t, j]$
 $r[t, j] : b = \lfloor j/32 \rfloor \mod 32768$
 $T[t, j] : t = j \mod 32$
 $T[t, j] \rightarrow j \mod 32$
 $S[t, i] \rightarrow imod 32$
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Advanced Use: CUDA/OpenCL Code Generation for (t = 0; t < T; t++) {

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B[i] = 0.33333 * (A[i-1] + A[i] + A[i + 1]);
for (j = 1; j < N - 1; j++)
A[j] = B[j];
filter on identifiers

$$S[t, i] \rightarrow [t]; t[t, j] \rightarrow [t]$$

filter on identifiers
 $T[t, j] \rightarrow [t]; t[t, j] \rightarrow [1]$
mark: kernel
 $0 \le b < 32768 \land 0 \le t < 32$
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 $S[t]/3 \rightarrow [t]/32 \rfloor$
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 $S[t]/3 \rightarrow [t]/3$

In final stages of scheduling, additional statements may need to be added

- Copy code
- Synchronization
- . . .

These additional statements depend on ancestors

- the statements should only be executed in a given part of the schedule tree
- iteration domains depend on outer schedule (e.g., data to be copied)
- \Rightarrow new "extension" node type
- \Rightarrow maps outer schedule dimensions to extra iteration domain

$$0 \leq b_{0}, b_{1} < 128 \land 0 \leq t_{0} < 32 \land 0 \leq t_{1} < 16$$

$$S_{0}[i, j] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128;$$

$$S_{1}[i, j, k] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128$$

$$[] \rightarrow \text{write}_C[u, v] : 0 \leq u, v \leq 4095 \land b_{0} = \lfloor u/32 \rfloor \land b_{1} = \lfloor v/32 \rfloor$$

$$\text{sequence}$$

$$S_{0}[i, j] \rightarrow [\lfloor i/32 \rfloor, \lfloor j/32 \rfloor];$$

$$S_{1}[i, j, k] \rightarrow [\lfloor i/32 \rfloor, \lfloor j/32 \rfloor];$$

$$S_{0}[i, j] \rightarrow [0]; S_{1}[i, j, k] \rightarrow [\lfloor k/32 \rfloor]$$

$$\text{write}_C[32b_{0} + t_{0}, v] : t_{1} = v \mod 16$$

$$\int_{1}^{1} (b_{1}, b_{2}) \rightarrow \text{sync}[];$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{sync}[];$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{read}_A[u, v] :$$

$$0 \leq u, v \leq 4095 \land b_{0} = \lfloor u/32 \rfloor \land i_{2} = \lfloor v/32 \rfloor;$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{read}_B[u, v] : \dots$$

$$0 \le b_{0}, b_{1} < 128 \land 0 \le t_{0} < 32 \land 0 \le t_{1} < 16$$

$$S_{0}[i, j] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128;$$

$$S_{1}[i, j, k] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128$$

$$\boxed{\left[\rightarrow \text{ write}_C[u, v] : 0 \le u, v \le 4095 \land b_{0} = \lfloor u/32 \rfloor \land b_{1} = \lfloor v/32 \rfloor\right]}$$

$$\text{sequence}$$

$$S_{0}[i, j] \Rightarrow [\lfloor i/32 \rfloor, \lfloor j/32 \rfloor];$$

$$S_{1}[i, j, k] \rightarrow [\lfloor i/32 \rfloor, \lfloor j/32 \rfloor];$$

$$S_{0}[i, j] \rightarrow [0]; S_{1}[i, j, k] \rightarrow [\lfloor k/32 \rfloor]$$

$$\text{write}_C[u, v] \Rightarrow [u, v]$$

$$\int_{[i, i, i, 2]} \Rightarrow \text{ sync}[];$$

$$\sum_{j \ge 4, v \le 4095 \land b_{0} = \lfloor u/32 \rfloor \land i_{2} = \lfloor v/32 \rfloor;$$

$$\sum_{j \ge 0, i_{1}, i_{2}} \rightarrow \text{ read}_B[u, v] : \dots$$

$$0 \leq b_{0}, b_{1} < 128 \land 0 \leq t_{0} < 32 \land 0 \leq t_{1} < 16$$

$$S_{0}[i, j] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128;$$

$$S_{1}[i, j, k] : b_{0} = \lfloor i/32 \rfloor \mod 128 \land b_{1} = \lfloor j/32 \rfloor \mod 128$$

$$[] \rightarrow \text{write}_C[u, v] : 0 \leq u, v \leq 4095 \land b_{0} = \lfloor u/32 \rfloor \land b_{1} = \lfloor v/32 \rfloor$$

$$sequence$$

$$S_{0}[i, j] \Rightarrow [\lfloor i/32 \rfloor, \lfloor j/32 \rfloor];$$
write_C[32b_{0} + t_{0}, v] : t_{1} = v \mod 16
$$S_{0}[i, j] \rightarrow [0]; S_{1}[i, j, k] \rightarrow [\lfloor k/32 \rfloor]$$
write_C[2u, v] \rightarrow [u, v]
$$S_{0}[i, j] \rightarrow [0]; S_{1}[i, j, k] \rightarrow [\lfloor k/32 \rfloor]$$

$$write_C[u, v] \rightarrow [u, v]$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{sync}[];$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{read}_A[u, v] :$$

$$0 \leq u, v \leq 4095 \land b_{0} = \lfloor u/32 \rfloor \land i_{2} = \lfloor v/32 \rfloor;$$

$$[i_{0}, i_{1}, i_{2}] \rightarrow \text{read}_B[u, v] : \dots$$

Outline

Introduction

- Example
- Single Statement
- Multiple Statements
- Schedule Trees

Advantages

- Useful in several contexts
- More natural
- More convenient
- More expressive
- Extensible



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Conclusion

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Exploit the tree nature of a schedule rather than encoding it in a flat representation

Schedule trees are

- useful in several contexts
- more natural
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- extensible

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Conclusion

Conclusion:

Exploit the tree nature of a schedule rather than encoding it in a flat representation

Schedule trees are

- useful in several contexts
- more natural
- more convenient
- more expressive
- extensible
- Future work
 - apply separation on schedule tree
 - additional node types
 - parametric tiling
 - clustering
 - . . .